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				Application Number	10/542576
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				First Named Inventor	Shinichi HORI
				Art Unit	4125
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Sheet 1 of 1				Attorney Docket Number	Q89211

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code <sup>2</sup> (if known)		
		US			
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation <sup>6</sup>
		Country Code <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
		JP	1-233813	A	09-19-1989		
		JP	6-342561	A	12-13-1994		
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation <sup>6</sup>
		F. Behbahani, et al. "A Broad-Band Tunable CMOS Channel-Select Filter for a Low-IF Wireless Receiver", IEEE, Journal of Solid-State Circuits, Vol. 35, No. 4, April 2000, pp 476-489.	
		B. Pankiewicz, et al. "A Field Programmable Analog Array for CMOS Continuous-Time OTA-A Filter Applications", IEEE, Journal of Solid-State Circuits, Vol. 37, No. 2, February 2002, pp 125-136.	

Examiner Signature		Date Considered	
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